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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/964,681	09/27/2001	Ray M. Richardson	INTL-0607-US (P11748)	2831
7590 10/19/2004			EXAMINER	
Timothy N. Trop TROP, PRUNER & HU, P.C. SUITE 100 8554 KATY FWY. HOUSTON, TX 77024-1805			NGUYEN, MIKE	
			ART UNIT	PAPER NUMBER
			2182	

DATE MAILED: 10/19/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/964,681

Applicant(s)

RICHARDSON, RAY M.

Examiner

Mike Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 03 August 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-30 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-30 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

  
FRITZ FLEMING  
PRIMARY EXAMINER  
GROUP 2100

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

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## DETAILED ACTION

### *Notices & Remarks*

1. Applicant's Amendment 08/03/2004 in response to Examiner's Office Action has been reviewed. The following rejections now apply.

2. Claims 1-30 are pending for the examination.

### *Claim Rejections - 35 USC § 102*

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1-30 are rejected under 35 U.S.C. 102(e) as being anticipated by Yeivin et al.

(U.S. Pat. No. 6,473,808 B1).

As to claim 21, Yeivin teaches a system (see fig. 3) comprising:

a processor (see fig. 3 element 90); and

a storage coupled to said processor storing instructions (see fig. 3 element 130 col. 7 lines

5-14) that enable the processor to:

initiate a direct memory access (see col. 15 lines 17-55); and

successively transfer data (see col. 17 lines 16-37) from linked buffers in a first processor system (see figs 1, 3 old first memory bank 1070 or first memory bank 70, old first processor 1090 or first processor 90) to linked buffers in a second processor system (see figs 1, 3

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old second memory bank 1110 or second memory bank 110, old second processor 1100 or second processor 100 and col. 3 line 33 to col. 4 line 24).

As to claim 22, Yeivin teaches the system of claim 21 wherein said storage stores instructions that enable the processor to successively transfer data from linked buffers arranged in linked list on the first processor system to buffers arranged in the linked list on a second processor system (see col. 17 lines 16-37 and col. 3 line 33 to col. 4 line 24).

As to claim 23, Yeivin teaches the system of claim 22 wherein said storage stores instructions that enable the processor to provide descriptors that indicate the status of each of said buffers (see col. 17 lines 31-37).

As to claim 24, Yeivin teaches the system of claim 23 wherein said storage stores instructions that enable the processor to provide flags that indicate whether a buffer is empty or full (see col. 17 lines 5-9).

As to claim 25, Yeivin teaches the system of claim 21 wherein said linked buffers are within a cellular telephone (see col. 1 lines 15-21 and col. 17 lines 16-37).

As to claim 26, Yeivin teaches the system of claim 25 wherein said processor is baseband processor, said system further including a multimedia processor (see ABSTRACT).

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As to claim 27, Yeivin teaches the system of claim 21 wherein said storage stores instructions that enable the processor to determine the status of a buffer to which data is to be transferred before transferring the data (see col. 7 lines 5-10 and col. 17 lines 5-9).

As to claim 28, Yeivin teaches the system of claim 27 wherein said storage stores instructions that enable the processor to determine if a buffer from which data is to be transferred is empty and if so, automatically fill the buffer with data (see col. 7 lines 5-10 and col. 17 lines 5-37).

As to claim 29, Yeivin teaches the system of claim 28 wherein said storage stores instructions that enable the processor to generate an interrupt when a buffer is empty and data is to be transferred from the buffer, intercept the interrupt, and automatically fill the buffer (see col. 7 lines 5-10 and col. 17 lines 5-37).

As to claim 30, Yeivin teaches the system of claim 29 wherein said storage stores instructions that enable the processor to determine whether a buffer that is to receive data is full and if the buffer is full, automatically generate an interrupt, intercept the interrupt, and automatically empty the buffer (see col. 7 lines 5-10 and col. 17 lines 5-37).

Claims 1-10 are directed to a method implementing the system as set forth in claims 21-30. Since Yeivin teaches the system as set forth in claims 21-30; therefore, he also teaches the method as set forth in claims 1-10.

Claims 11-20 are directed to an article implementing the system as set forth in claims 21-30. Since Yeivin teaches the system as set forth in claims 21-30; therefore, he also teaches the article as set forth in claims 11-20.

***Response to Arguments***

5. Applicant's arguments with respect to claim 1-30 have been considered but are moot in view of the new ground(s) of rejection.

***Conclusion***

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mike Nguyen whose telephone number is 571 272-4153. The examiner can normally be reached on 8:00AM-4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey Gaffin can be reached on 571 272-4146. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

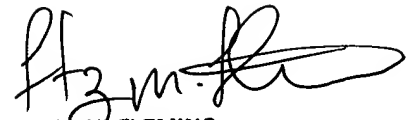
Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Mike Nguyen  
Patent Examiner  
Group Art Unit 2182  
10/15/2004

A handwritten signature in black ink, appearing to read 'Fritz Fleming', is written over a rectangular stamp.

FRITZ FLEMING  
PRIMARY EXAMINER  
GROUP 2100